	Туре	L#	Hits	Search Text	DBs	Time Stamp	Comments
4	BRS	L1	2099	fetch\$5 with word with instruction	1 -	200 <i>4</i> /11/29 16:18	
2	BRS	L2	1536	1 and (memory same processor)		2004/11/29 16:07	
3	BRS	L3	13	2 and (divid\$5 with word with length)		2004/11/29 16:19	
4	BRS	L4	4734783	2 and (divid\$5 with word with length) anf type	1 .	200 <i>4</i> /11/29 16:08	
5	BRS	L5	13	2 and (divid\$5 with word with length) and type		200 <i>4</i> /11/29 16:11	
6	BRS	L6	4	5 and (address with sequential)		200 <i>4</i> /11/29 16:19	

	Туре	L#	Hits	Search Text	DBs	Time Stamp	Comments
7	BRS	L7	8	2 and (divid\$5 with word with length) and size		200 <i>4</i> /11/29 16:19	
8	BRS	L8	0	7 not 5		2004/11/29 16:12	
9	BRS	L9	6	7 and half		2004/11/29 16:13	
10	BRS	L10	2	7 and half and "OR"		2004/11/29 16:14	
11	BRS	L11	526	2 and half and "OR"		2004/11/29 16:14	
12	BRS	L12	197	11 and multiplexer and flip		2004/11/29 16:15	

	Туре	L#	Hits	Search Text	DBs	Time Stamp	Comments
13	BRS	L13	6	12 and full with word with length			
14	IS&R	L14	549	(712/204-206).CCLS.	US- PGPUB; USPAT; EPO; JPO; DERWEN T; IBM_TDB		
15	BRS	L15	98	14 and 1			
16	BRS	L16	81	14 and 2			
17	BRS	L17	3	16 and (divid\$5 with word with length)			
18	BRS	L18	28	16 and (address with sequential)			

	Туре	L#	Hits	Search Text	DBs	Time Stamp	Comments
19	BRS	L19	3	16 and (divid\$5 with word with length) and size	1 '		